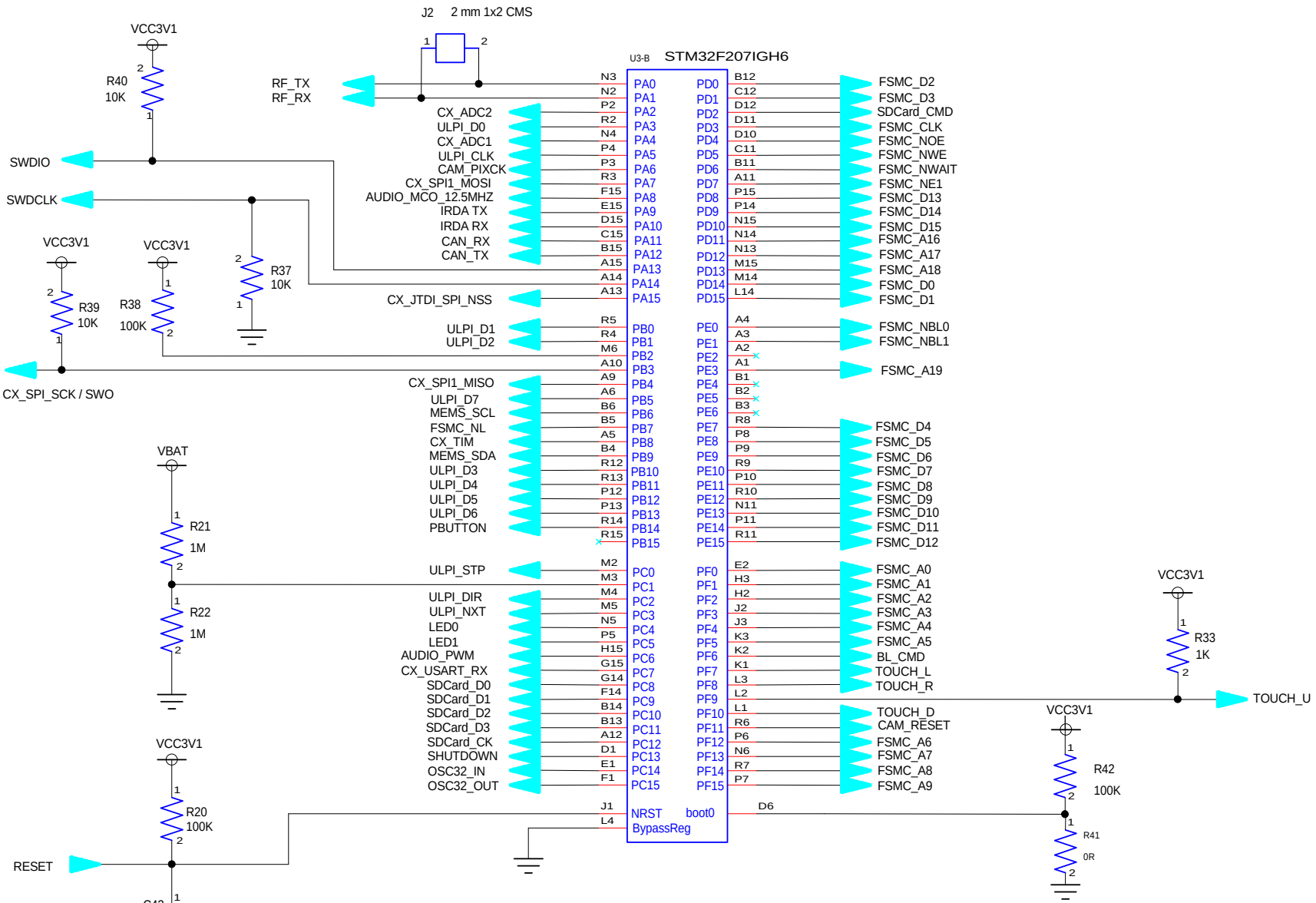
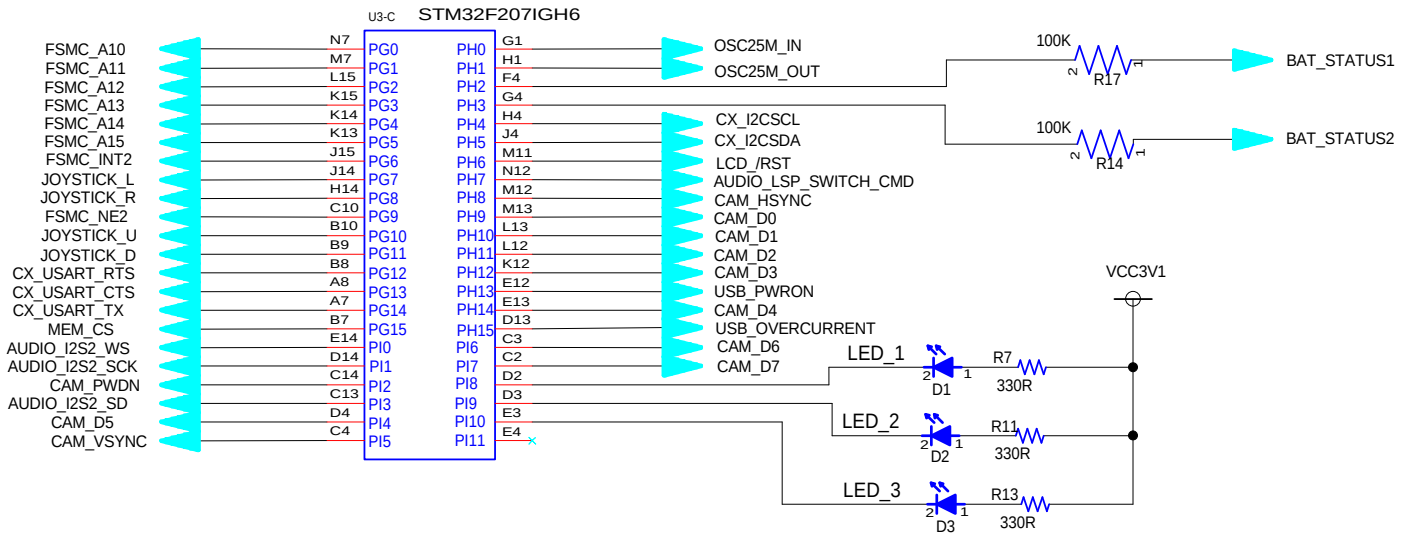
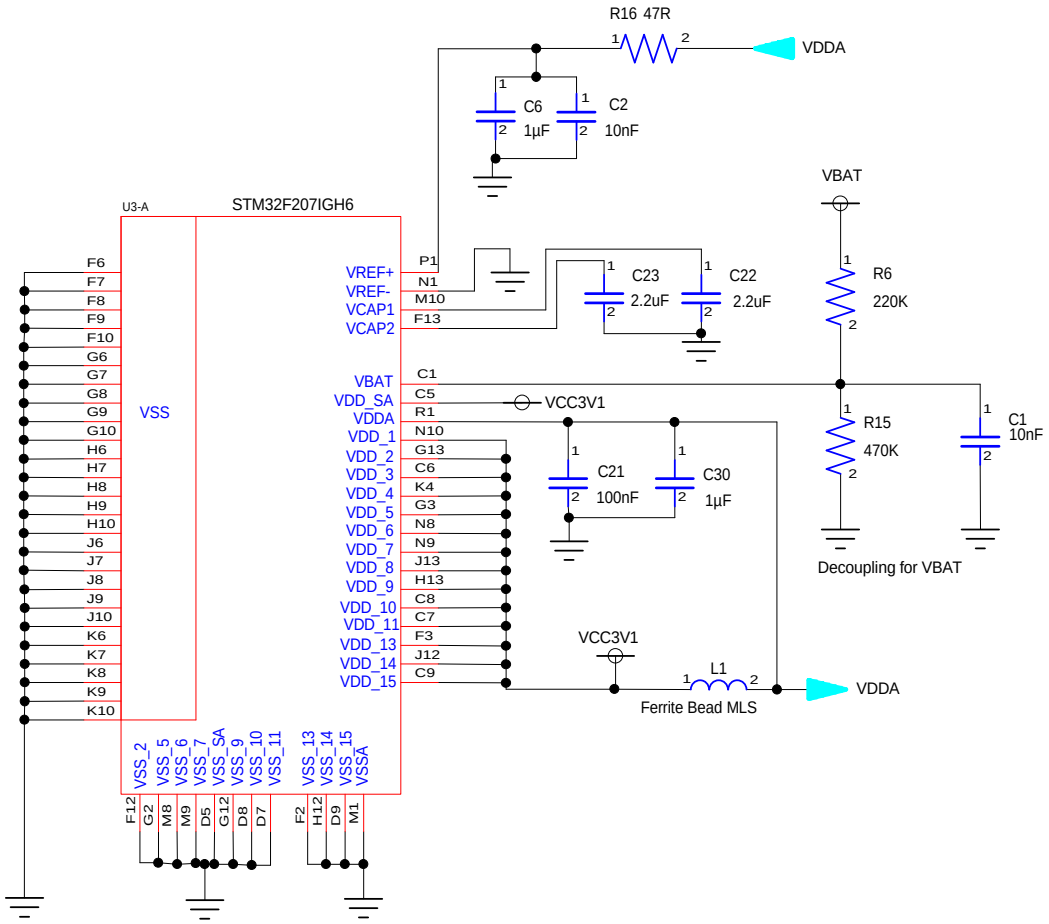
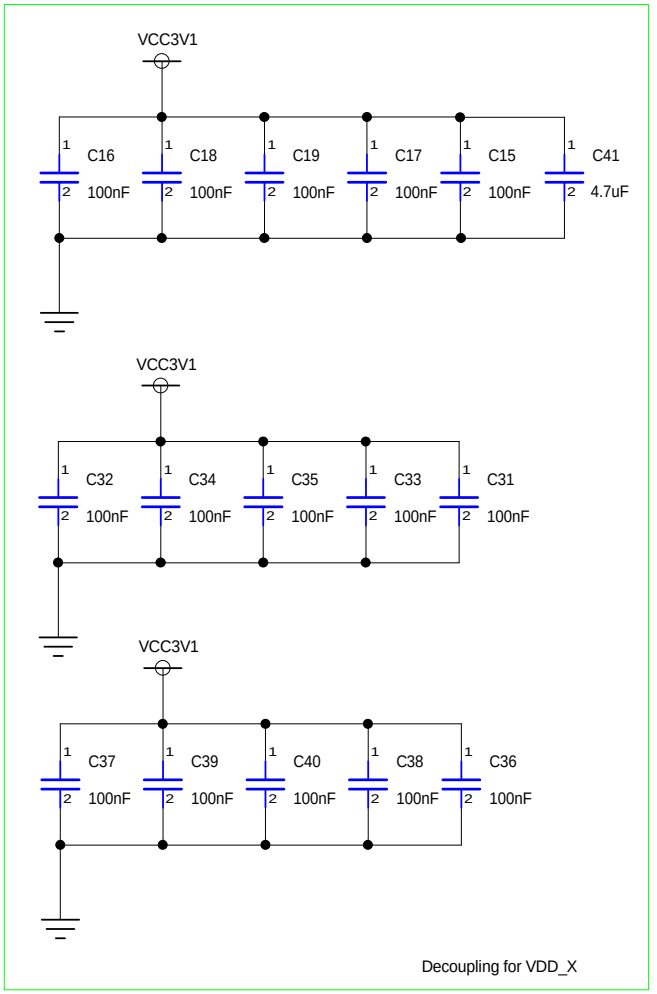


CONTRACT NO.		COMPANY NAME	RAISONANCE SAS
APPROVALS	DATE	DWG	Target Board Open4 * OP4-STM32F207G *
DRAWN LP	09/2011	SIZE A / FSCM NO. OP4-STM3220G / DWG NO. MCU 1/2	REV. V1.2
CHECKED YRT	09/2011		
ISSUED		SCALE	SHEET 1 of 11

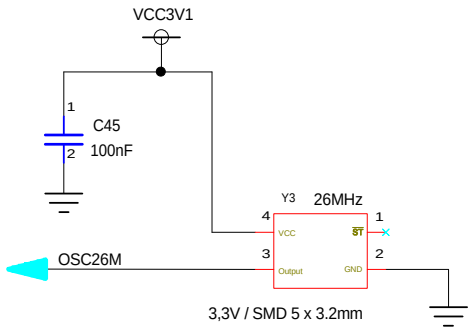


CONTRACT NO.		COMPANY NAME RAISONANCE SAS	
APPROVALS	DATE	DWG Target Board Open4 * OP4-STM32F207G *	
DRAWN LP	09/2011	SIZE A	FSCM NO. OP4-STM3220G
CHECKED YRT	09/2011	DWG NO. MCU 2/2	REV. V1.2
ISSUED		SCALE	SHEET 2 of 11

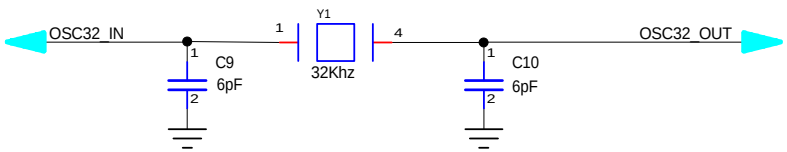




CONTRACT NO.		COMPANY NAME RAISONANCE SAS	
APPROVALS	DATE	DWG Target Board Open4 * OP4-STM32F207G *	
DRAWN LP	09/2011	SIZE A	FSCM NO. OP4-STM3220G
CHECKED YRT	09/2011	DWG NO. MCU Power	REV. V1.2
ISSUED		SCALE	SHEET 3 of 11

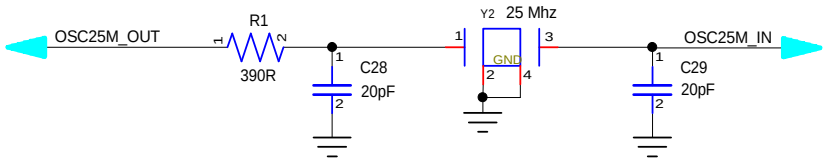


USB PHY and camera clock oscillator



Default capacity value for 32Khz crystal : 6pF
Final value will depend on available crystal for manufacturing

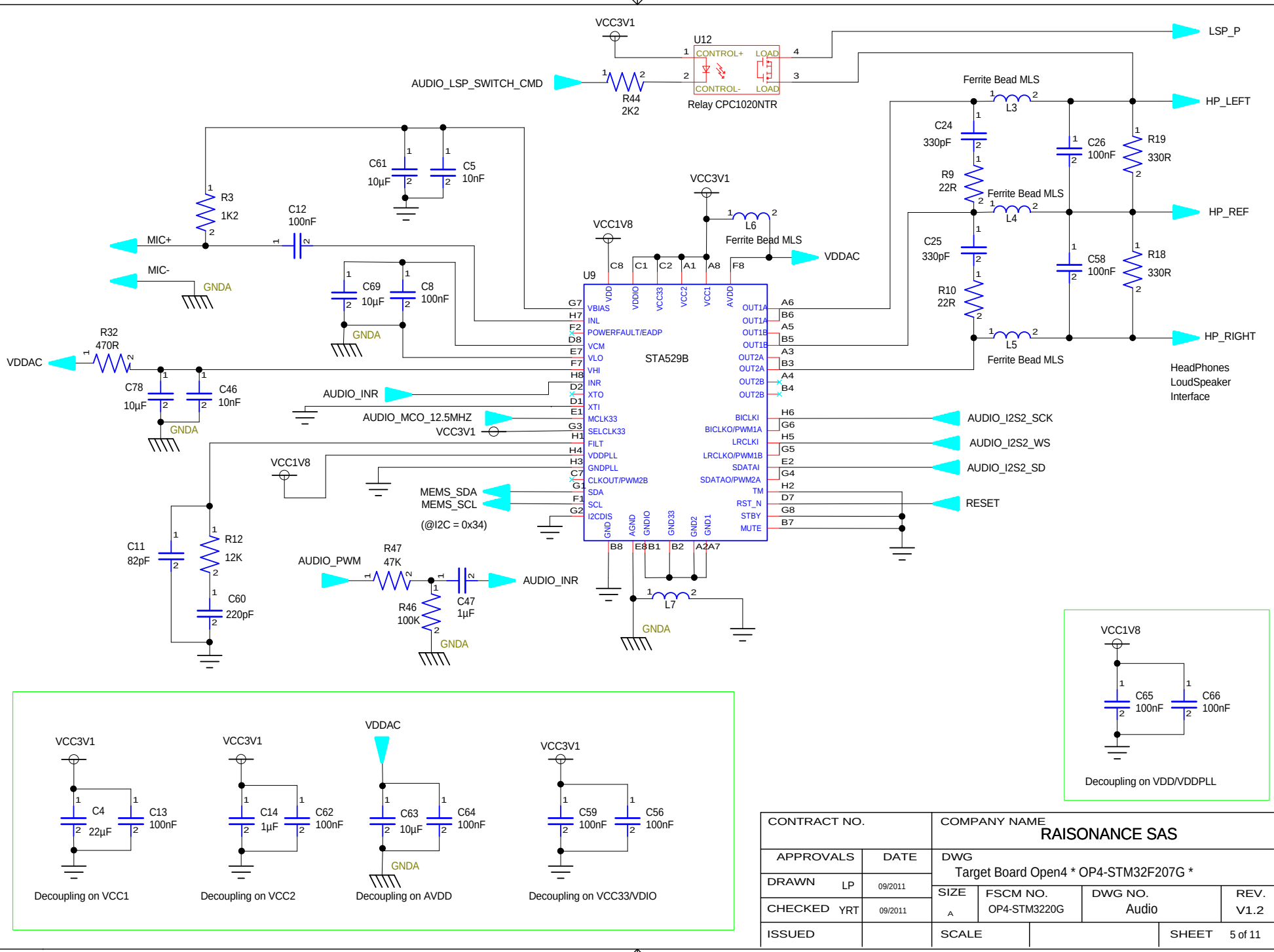
RTC clock oscillator



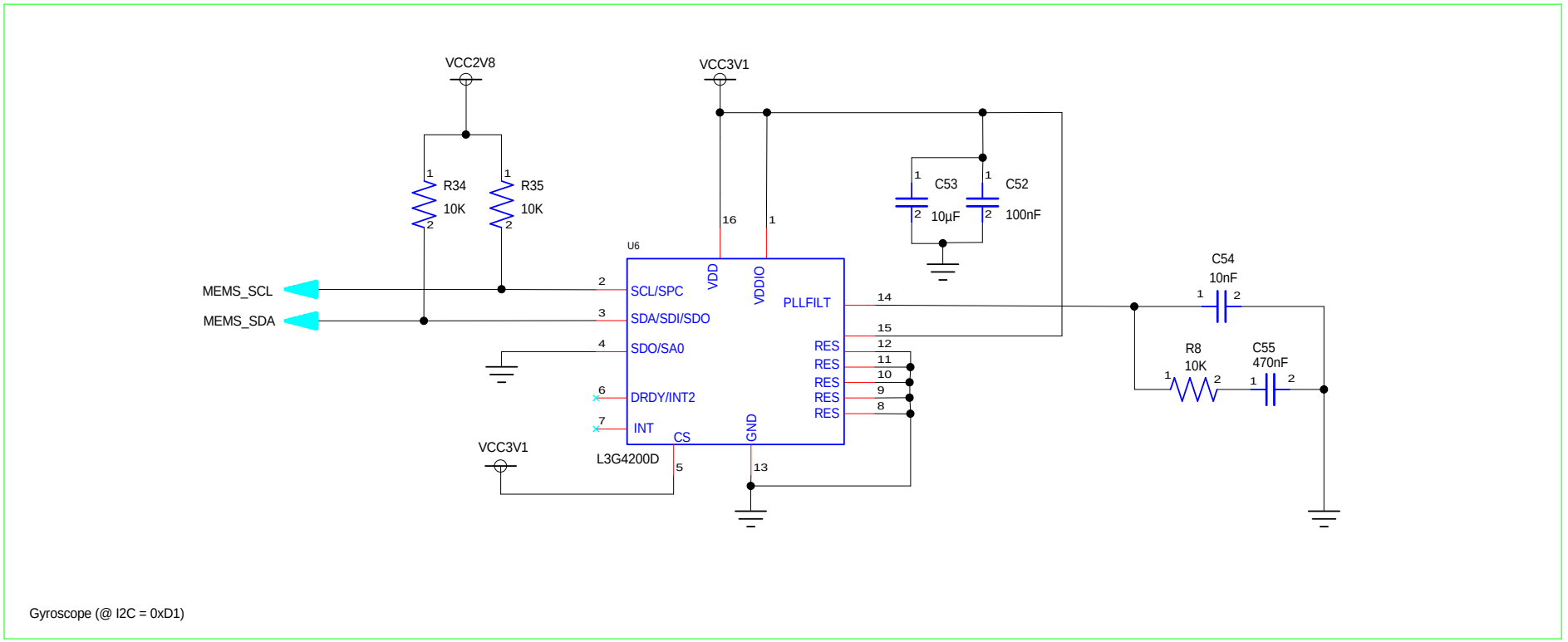
Default capacity value for 25MHz crystal : 20pF
Final value will depend on available crystal for manufacturing

CPU clock oscillator

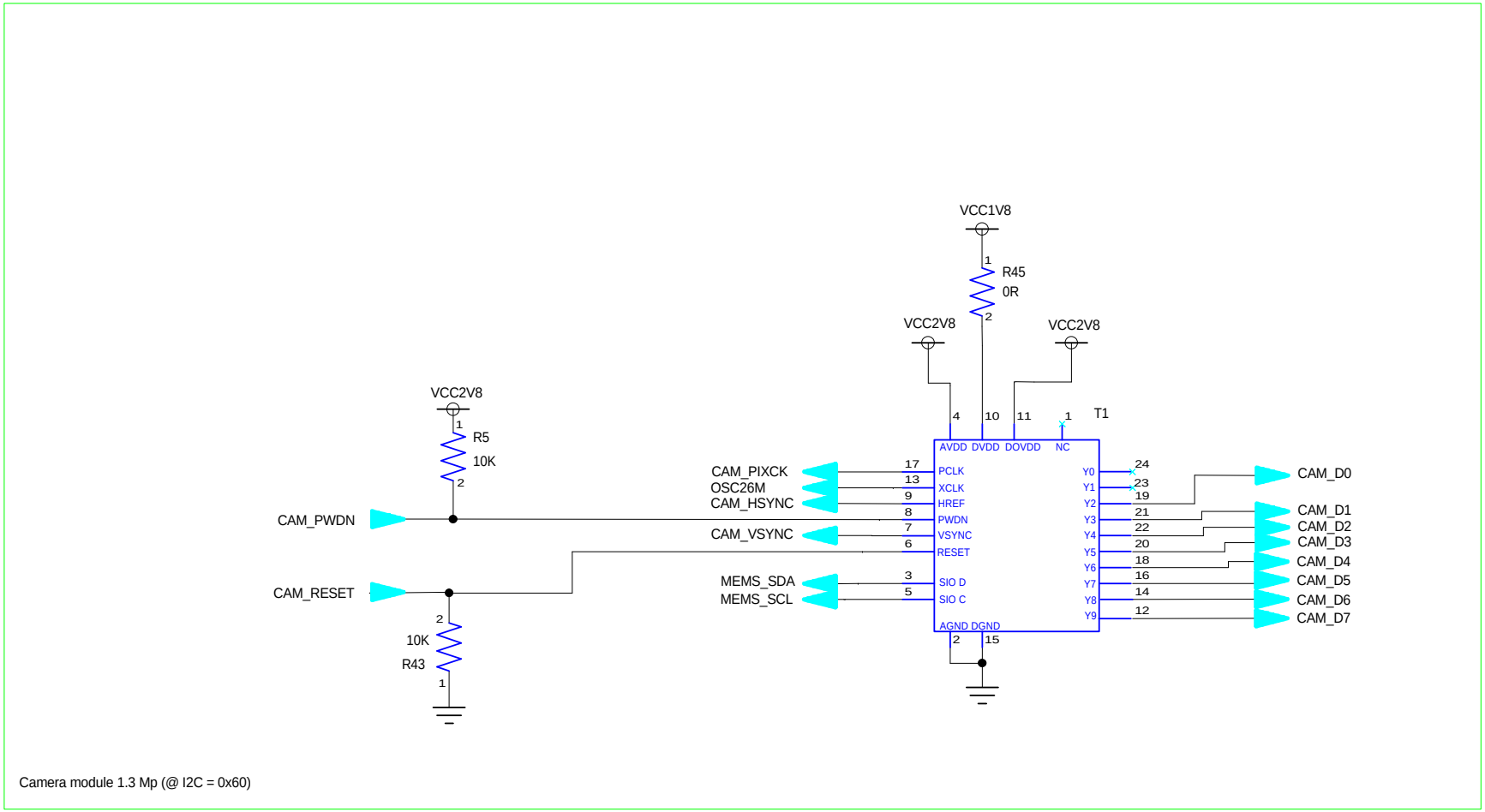
CONTRACT NO.		COMPANY NAME RAISONANCE SAS	
APPROVALS	DATE	DWG Target Board Open4 * OP4-STM32F207G *	
DRAWN LP	09/2011	SIZE A	FSCM NO. OP4-STM3220G
CHECKED YRT	09/2011	DWG NO. Clock sources	REV. V1.2
ISSUED		SCALE	SHEET 4 of 11



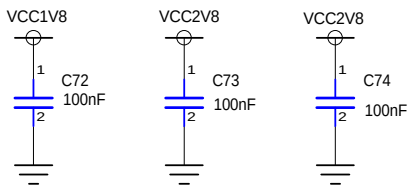
CONTRACT NO.		COMPANY NAME	RAISONANCE SAS
APPROVALS	DATE	DWG	Target Board Open4 * OP4-STM32F207G *
DRAWN LP	09/2011	SIZE A	FSCM NO. OP4-STM3220G / DWG NO. Audio / REV. V1.2
CHECKED YRT	09/2011		
ISSUED		SCALE	SHEET 5 of 11



CONTRACT NO.		COMPANY NAME RAISONANCE SAS	
APPROVALS	DATE	DWG Target Board Open4 * OP4-STM32F207G *	
DRAWN LP	09/2011		
CHECKED YRT	09/2011	SIZE A / FSCM NO. OP4-STM3220G / DWG NO. Gyroscope / REV. V1.2	
ISSUED		SCALE	SHEET 6 of 11

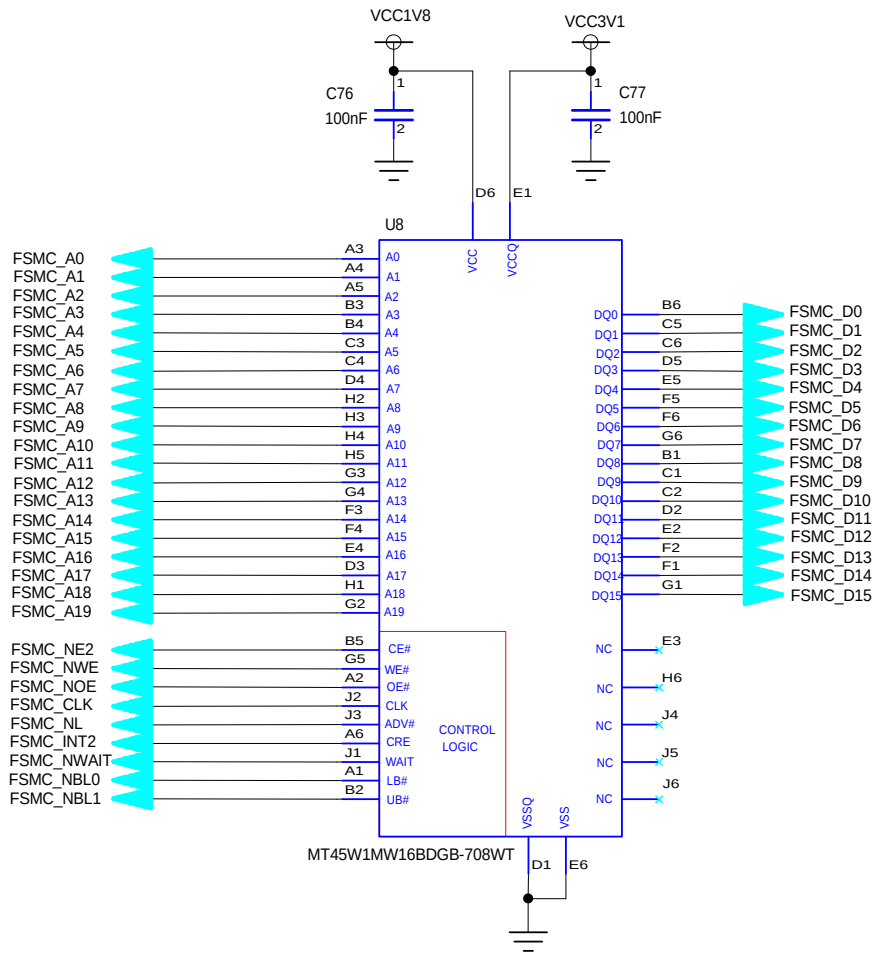


Camera module 1.3 Mp (@ I2C = 0x60)

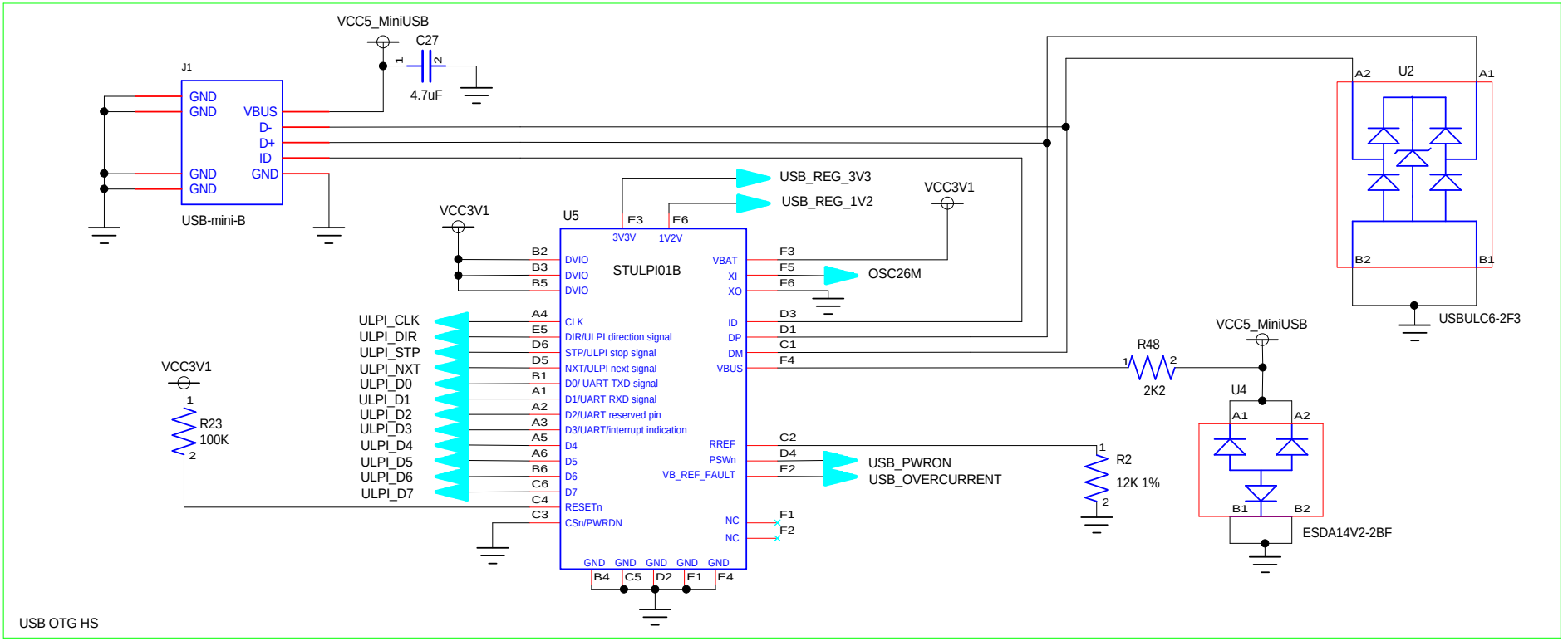


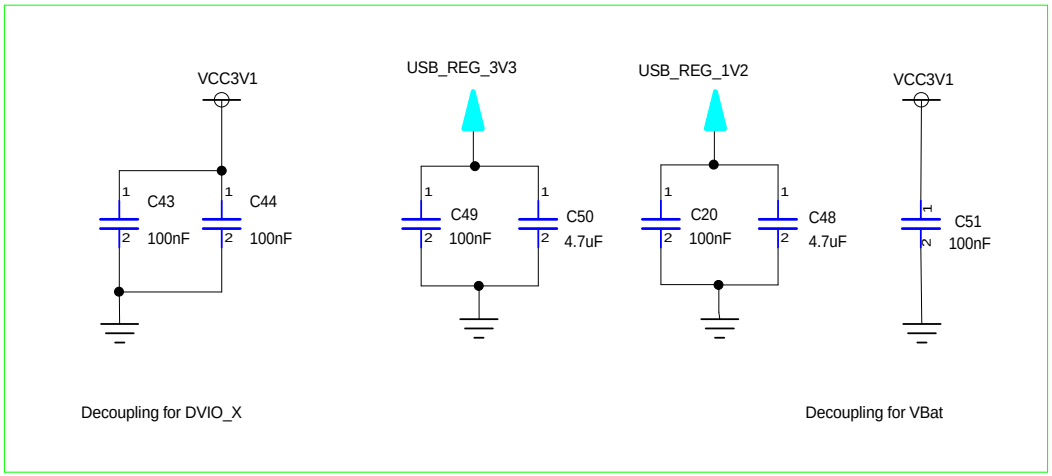
Decoupling for VDD_X

CONTRACT NO.		COMPANY NAME RAISONANCE SAS	
APPROVALS	DATE	DWG Target Board Open4 * OP4-STM32F207G *	
DRAWN LP	09/2011	SIZE A	FSCM NO. OP4-STM3220G
CHECKED YRT	09/2011	DWG NO. Camera	REV. V1.2
ISSUED		SCALE	SHEET 7 of 11

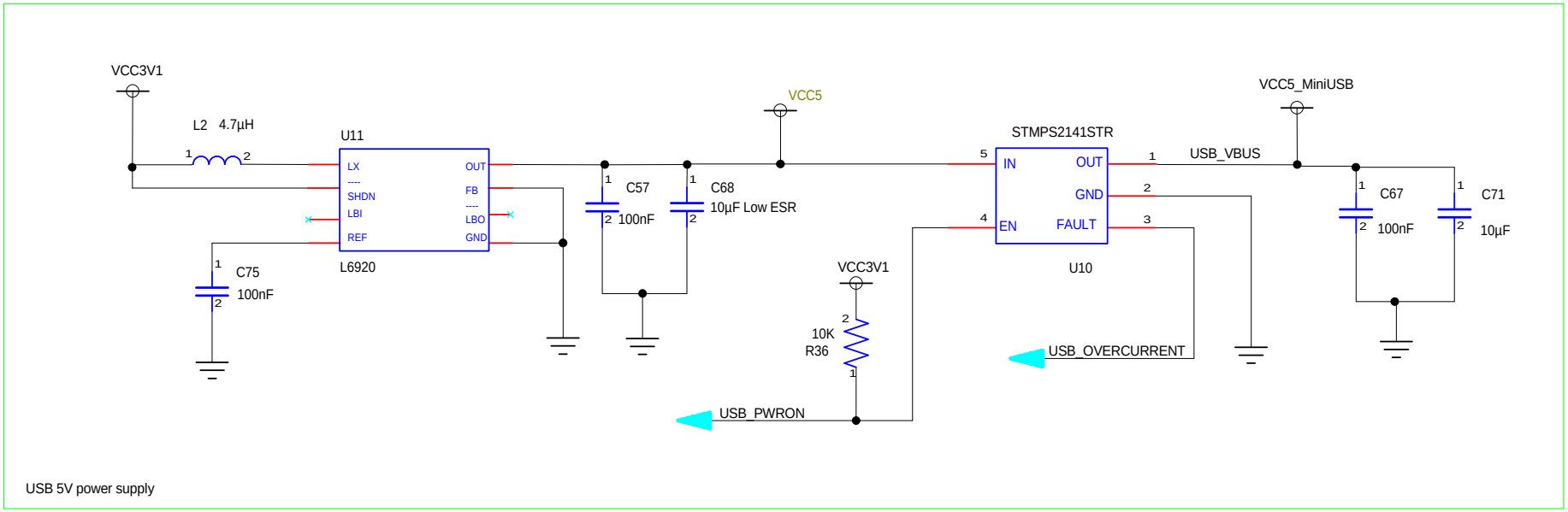


CONTRACT NO.		COMPANY NAME RAISONANCE SAS	
APPROVALS	DATE	DWG Target Board Open4 * OP4-STM32F207G *	
DRAWN LP	09/2011	SIZE A	FSCM NO. OP4-STM3220G
CHECKED YRT	09/2011	DWG NO. PSRAM	REV. V1.2
ISSUED		SCALE	SHEET 8 of 11

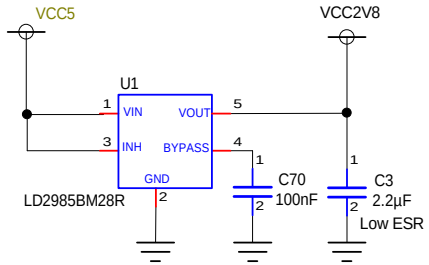




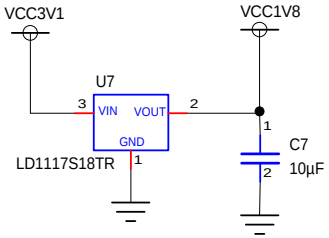
CONTRACT NO.		COMPANY NAME RAISONANCE SAS	
APPROVALS	DATE	DWG Target Board Open4 * OP4-STM32F207G *	
DRAWN LP	09/2011	SIZE A / FSCM NO. OP4-STM3220G / DWG NO. USB Peripheral	REV. V1.2
CHECKED YRT	09/2011		
ISSUED		SCALE	SHEET 9 of 11



USB 5V power supply

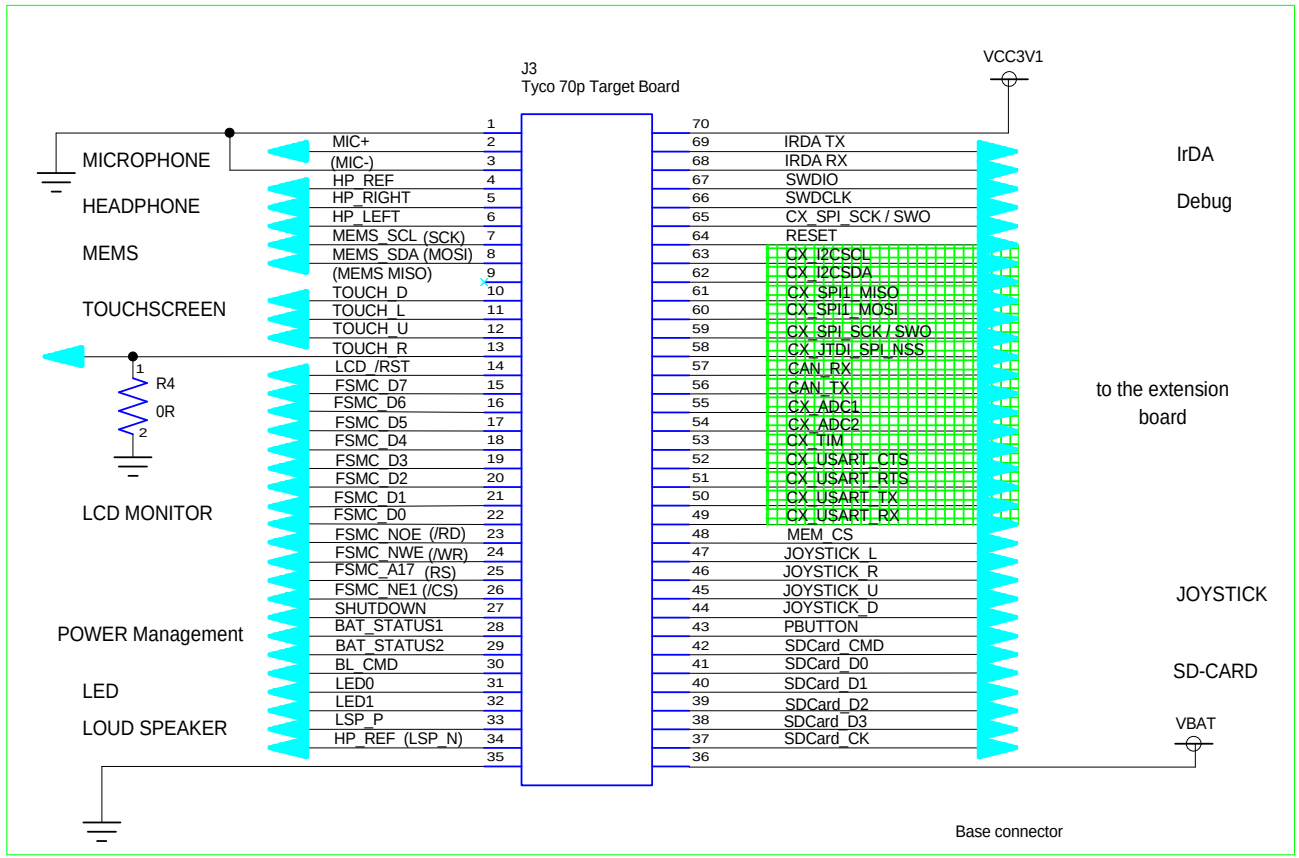


2V8 power supply



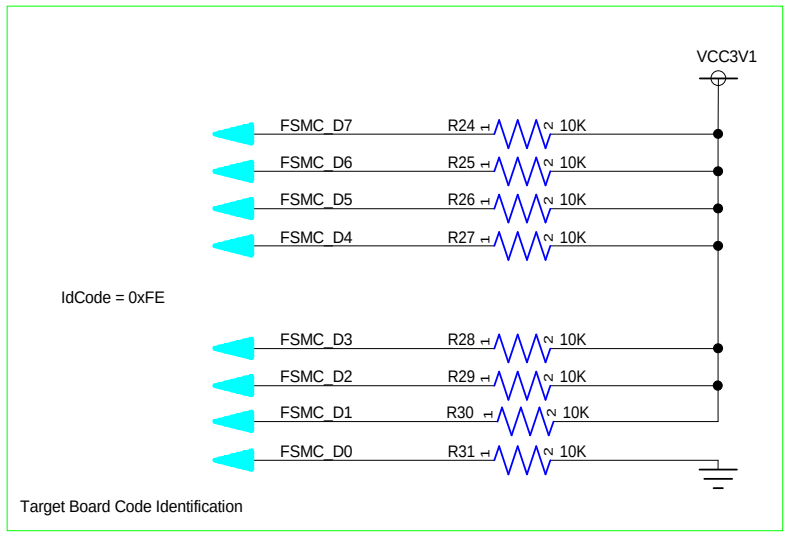
1V8 power supply

CONTRACT NO.		COMPANY NAME RAISONANCE SAS	
APPROVALS	DATE	DWG Target Board Open4 * OP4-STM32F207G *	
DRAWN LP	09/2011	SIZE A	FSCM NO. OP4-STM3220G
CHECKED YRT	09/2011	DWG NO. Power supplies	REV. V1.2
ISSUED		SCALE	SHEET 10 of 11



Extension Board connector

Daughter Board printed name:	JP2 Extension connector:	J6 Target Board connector:	Target Board signals:
V2V8	1	70	VCC3V1
GND	2	1	GND
SCL	3	63	CX_I2CSCL
SDA	4	62	CX_I2CSDA
MISO	5	61	CX_SPI1_MISO
SD	6	60	CX_SPI1_MOSI
SCK	7	59	CX_SPI1CK_SWO
WS	8	58	CX_SPI1_NSS
CANH	9	57	CAN_RX
CANL	10	56	CAN_TX
ADC1	11	55	CX_ADC1
ADC2	12	54	CX_ADC2
A_TIM	13	53	CX_TIM
CTS	14	52	CX_USART_CTS
RTS	15	51	CX_USART_RTS
TX	16	50	CX_USART_TX
CK	17	43	P_BUTTON
RX	18	49	CX_USART_RX
VEXT	19	36	VBAT
GND	20	35	GND



CONTRACT NO.		COMPANY NAME RAISONANCE SAS	
APPROVALS	DATE	DWG Target Board Open4 * OP4-STM32F207G *	
DRAWN LP	09/2011	SIZE A	FSCM NO. OP4-STM3220G
CHECKED YRT	09/2011	DWG NO. Base connector	REV. V1.2
ISSUED		SCALE	SHEET 11 of 11